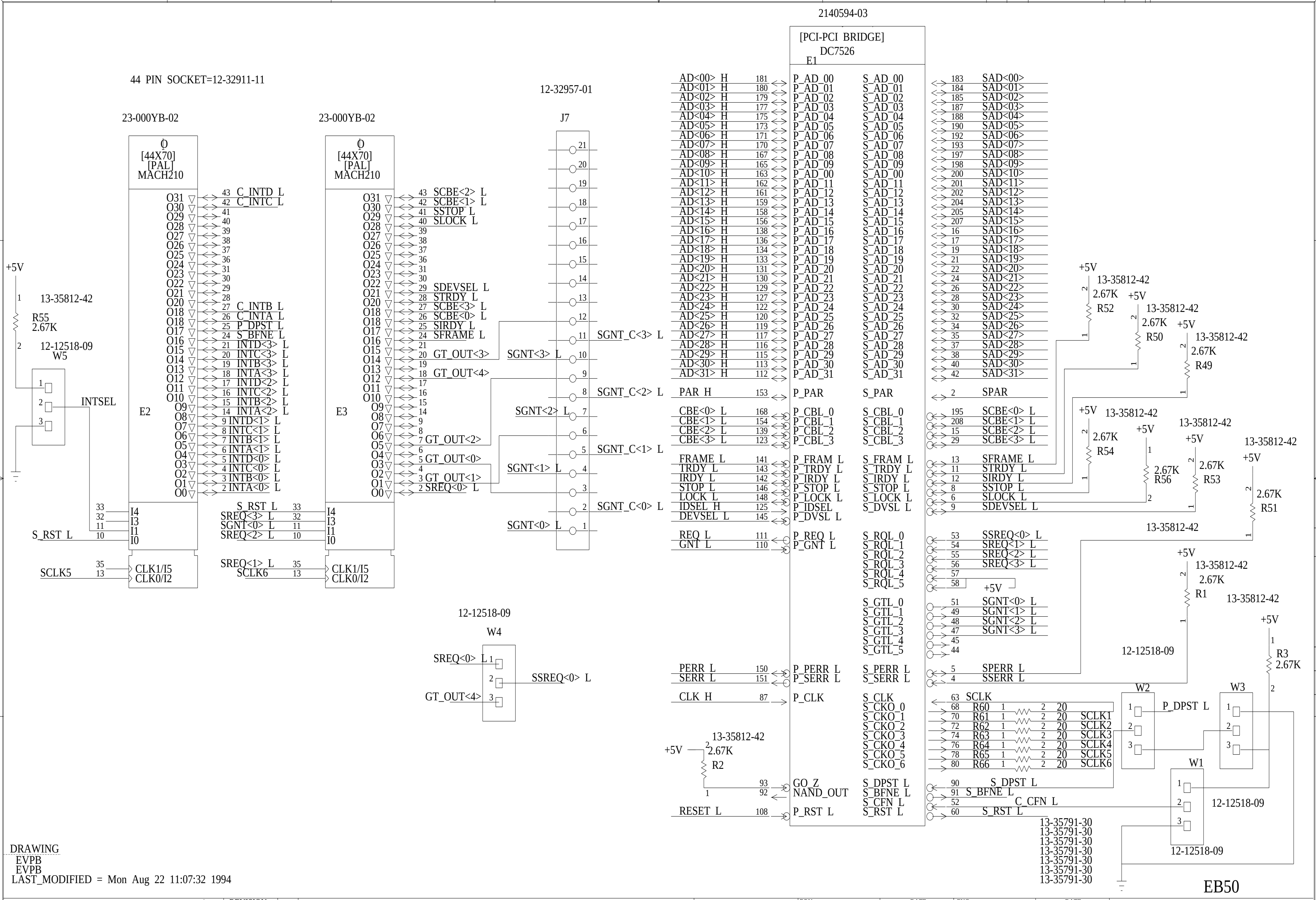


EC-QAL8A-TE

REVISION		
CHK	CHANGE NO.	REV

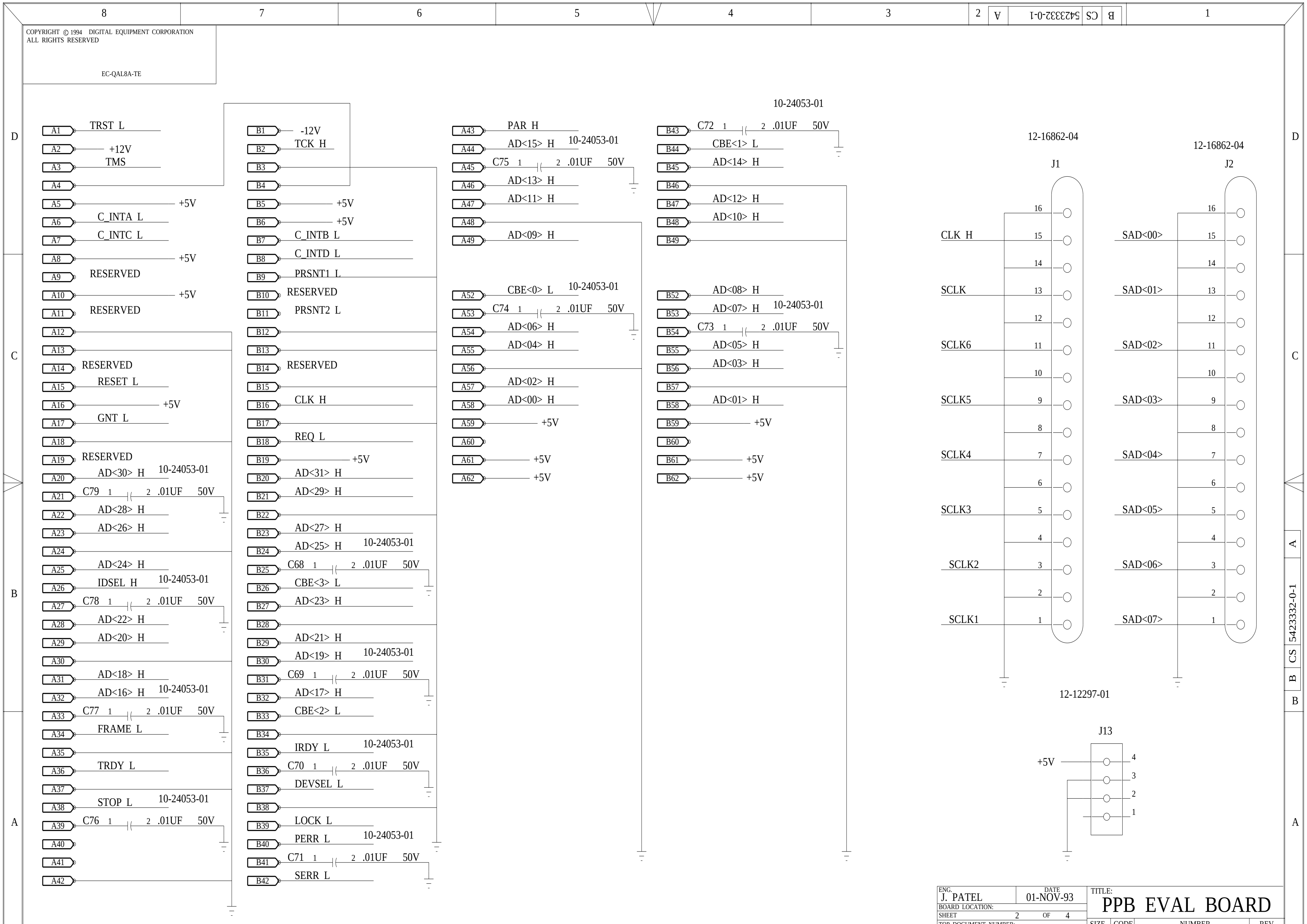
digital

DRN. J. PATEL — DATE 01-NOV-93
CHK'D. — DATE
ENG. J. PATEL — DATE 01-NOV-93
BOARD LOCATION:
SHEET 1 OF 4
TOP DOCUMENT NUMBER: K-DD-5423332-0-0
USRA:
FIRST USED ON OPTION/MODEL:

TITLE: PPB EVAL BOARD

SIZE B — CODE CS — NUMBER 5423332-0-1 — REV. AX09

EC-QAL8A-TE

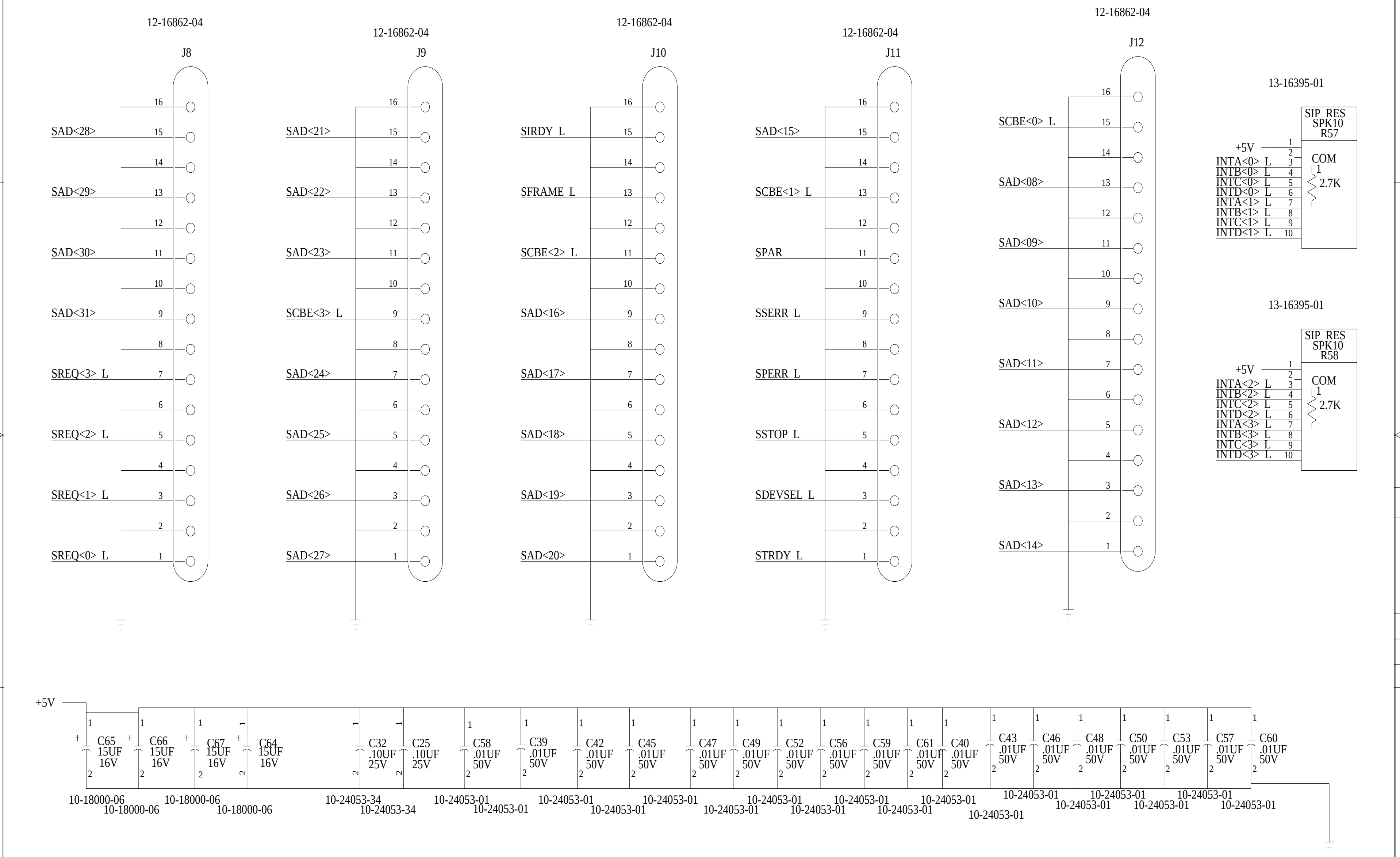


ENG. J. PATEL	DATE 01-NOV-93	TITLE: PPB EVAL BOARD	
BOARD LOCATION:			
SHEET 2 OF 4			
TOP DOCUMENT NUMBER: K-DD-5423332-0-0	SIZE B	CODE CS	NUMBER 5423332-0-1	REV. AX09

EC-QAL8A-TE

ENG. J. PATEL	DATE 01-NOV-93	TITLE: PPB EVAL BOARD	
BOARD LOCATION:			
SHEET 3 OF 4			
TOP DOCUMENT NUMBER: K-DD-5423332-0-0	SIZE B	CODE CS	NUMBER 5423332-0-1	REV. AX09

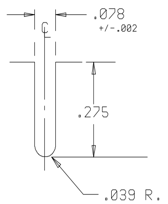
EC-QAL8A-TE



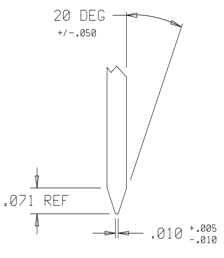
ENG. J. PATEL	DATE 01-NOV-93	TITLE: PPB EVAL BOARD	
BOARD LOCATION:			
SHEET 4 OF 4			
TOP DOCUMENT NUMBER: K-DD-5423332-0-0	SIZE B	CODE CS	NUMBER 5423332-0-1	REV. AX09

DATE	RESPONSIBLE ENGINEER	ECO NUMBER	REV

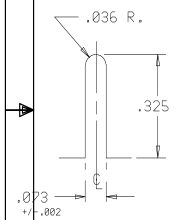
VIEWED FROM SIDE 1



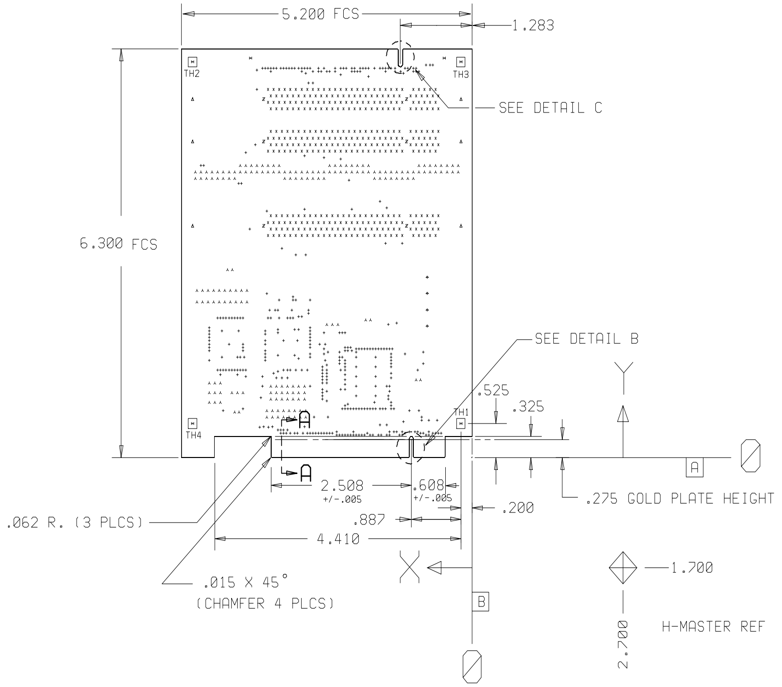
DETAIL C
NO SCALE



SECTION A-A
NO SCALE



DETAIL B
NO SCALE



NOTES

1. ALL ARTWORK FEATURES, IN BOTH DATA AND FILM, ARE SUPPLIED AT NOMINAL DIMENSIONS. UNLESS OTHERWISE SPECIFIED THE FINISHED CIRCUIT SHOULD REFLECT THESE DIMENSIONS, INCLUDING ALLOWABLE FABRICATION TOLERANCES PER DEC STD-176. SOLDERMASK FEATURES ARE SUPPLIED AT MINIMUM SIZES REFLECTING DESIRED COVERAGE. THESE FEATURES MAY BE MODIFIED TO ACCOMODATE SPECIFIC PROCESSES. REFERENCE DEC STD-176 FOR ASSOCIATED REQUIREMENTS.
2. VENDOR TO NICKEL AND GOLD PLATE FINGERS PER DEC STD 176.
3. ALL .015 DIA HOLES TO BE DRILLED AT .015 DIA MIN.

HI-POT TEST COORDINATES
ALL COORDINATES FROM H-MASTER ZERO

X COORD	Y COORD
POWER	
3.250	2.100
GROUND	
3.475	2.050

HOLE DATA (FOR STANDARD HOLE TOLERANCES USE DEC STD 176)

SYMBOL	
FINISHED HOLE SIZE (INCHES)	
POSITIVE TOLERANCE (INCHES)	
NEGATIVE TOLERANCE (INCHES)	
FINISHED HOLE SIZE (mm)	
POSITIVE TOLERANCE (mm)	
NEGATIVE TOLERANCE (mm)	
PLATED?	
QUANTITY	

FROM LAYER ______ TO LAYER ______

FABRICATION INFORMATION — MATERIAL AND WORKMANSHIP FOR ALL FABRICATED PRINTED WIRING BOARDS MUST MEET THE REQUIREMENTS OF DEC STD 176

FABRICATE BOARD PER: ______ AS SHOWN X | STEP AND REPEAT CODE: NONE | PRESS FIT COMPONENTS: YES ___ NO X

DESIGN INFORMATION

- NUMBER OF LAYERS: 4 LAYER CONSTRUCTION: C-MD-ELEN630-8-0 AS SHOWN ___
- CONTROLLED IMPEDANCE: YES ___ NO X CONTROLLED CAPACITANCE: YES ___ NO X
- SURFACE MOUNT TECH / MINIMUM PITCH: SIDE 1 .025 SIDE 2 >.050
- PLANAR RESISTOR TECHNOLOGY: YES ___ NO X NUMBER OF PRT LAYERS ___
- BOARD FINISH: REFLOW TIN LEAD ___ HASL X ALL COPPER ___ OTHER ___
- SOLDERMASK: NONE ___ SIDE 1 X SIDE 2 X SMOBC: YES X NO ___
- NOMENCLATURE: NONE ___ SIDE 1 X SIDE 2 X TENTED VIAS: YES ___ NO X
- MINIMUM VIA HOLE SIZE: .015 MINIMUM COMPONENT HOLE SIZE: .038
- PAD EXPANSION: OUTER LAYERS X SIGNAL INNER LAYERS ___ NONE ___
- INCIRCUIT THIEVING: NONE ___ SIDE 1 X SIDE 2 X AS SHOWN
- TECHNOLOGY (LW/LS): LINE .006 SPACE .007
- CIRCUIT OUTLINE/FINGER DETAIL: ______ AS SHOWN X
- SPECIAL REQUIREMENTS: ______

TOLERANCES
INCHES UNLESS OTHERWISE SPECIFIED

INCHES	mm	ANGLES
.XXX ± .010	,XX ± ,25	±0 DEG
.XX ± .020	,X ± ,50	30 MIN
.X ± .100	X, ± 2,50	

APPROVALS	DATE
DRAWN: J. CUNNINGHAM	27JUL94
CHECKED: W. SUPINSKI	27JUL94
MECH ENG:	
PROJ ENG:	
MFG ENG:	

SCALE: 1-1
SHEET 1 OF 2
TOP DOC NO: K-DD-5023331-0-0

digital™

TITLE:

CIRCUIT DRILL AND ETCH DRAWING

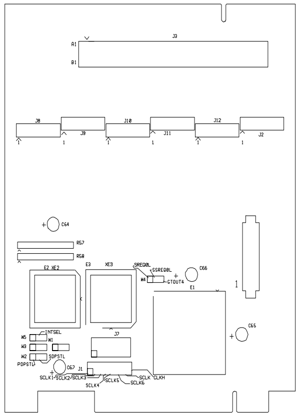
SIZE	CODE	NUMBER	REV
D	MD	5023331-0-0	AX04

ETCH REV: C1 — MEDIA= K

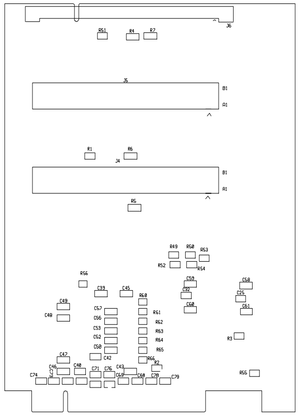
WO# 298173

8	7	6	5	4	3	2	1

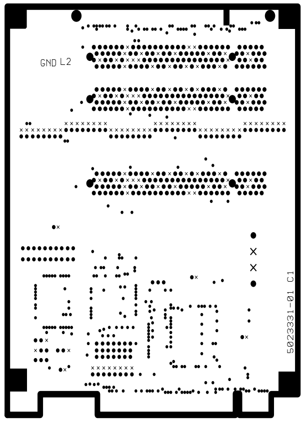
SILK SCREEN VIEWED FROM SIDE 1



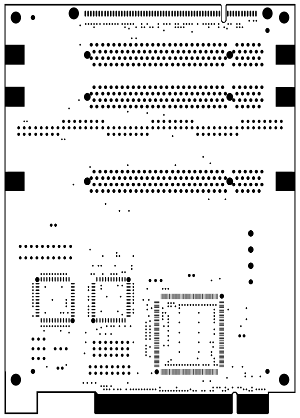
SILK SCREEN VIEWED FROM SIDE 2



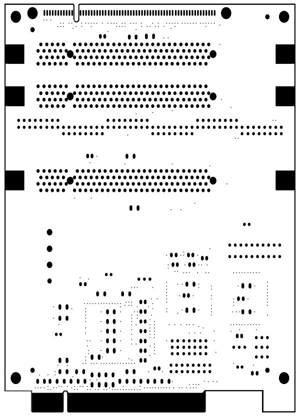
LAYER 2 VIEWED FROM SIDE 1



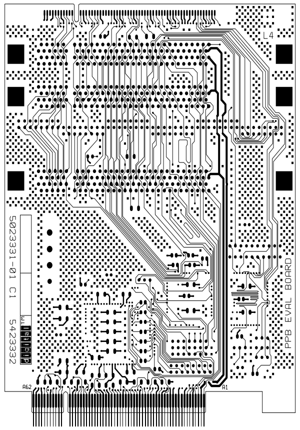
SOLDERMASK SIDE 1 VIEWED FROM SIDE 1



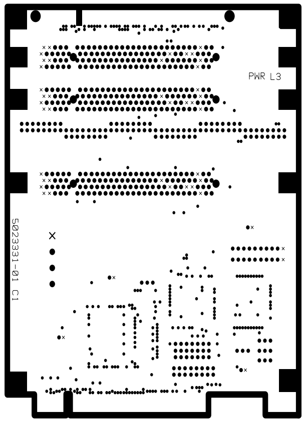
SOLDERMASK SIDE 2 VIEWED FROM SIDE 2



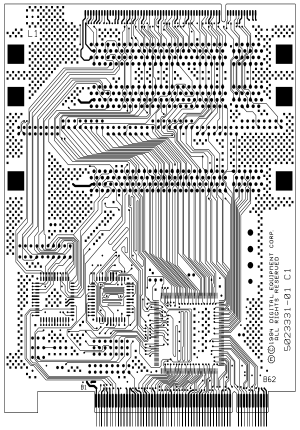
LAYER 4 VIEWED FROM SIDE 2



LAYER 3 VIEWED FROM SIDE 2



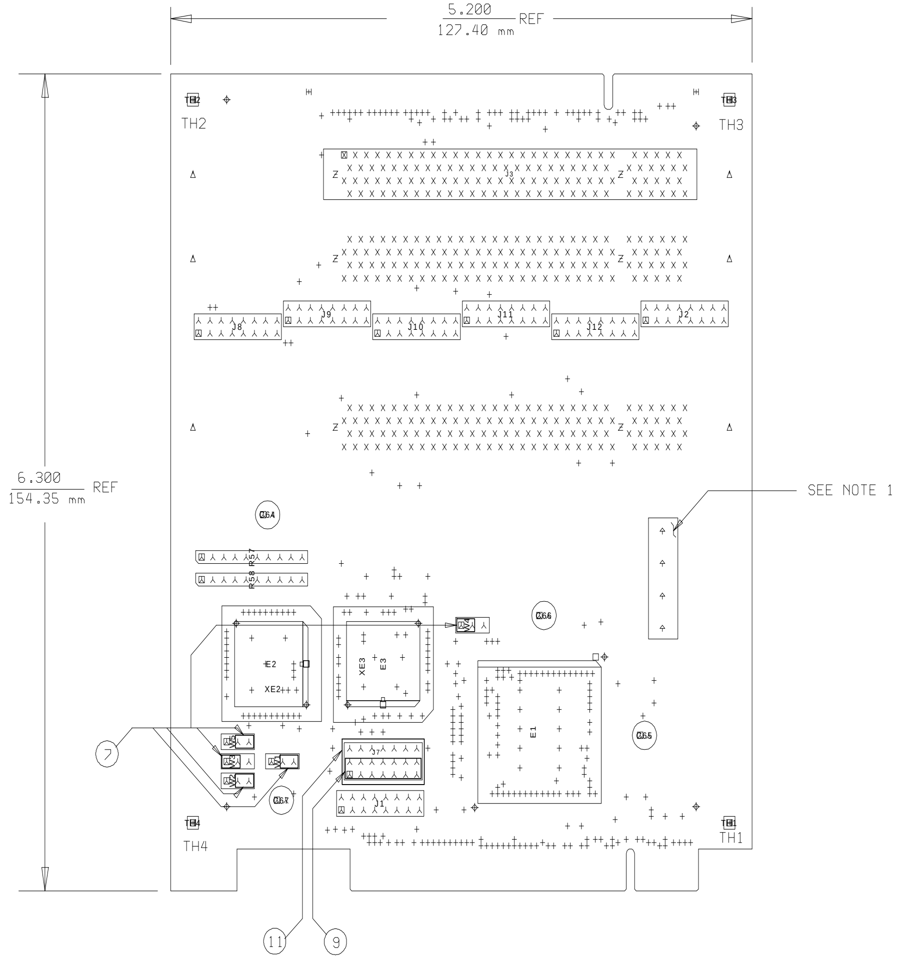
LAYER 1 VIEWED FROM SIDE 1



TITLE	DOCUMENT NUMBER			
CIRCUIT DRILL AND ETCH DRAWING	SIZE	CODE	NUMBER	REV
	D	MD	5023331-0-0	AX04
	SCALE 1-1	MEDIA= K	SHEET 2 OF 2	

1 WO# 298173

VIEWED FROM SIDE 1



DATE	RESPONSIBLE ENGINEER	ECO NO	REV

DATE	RESPONSIBLE ENGINEER	ECO NO	REV

NOTES:
1. OPTIONAL 4 PIN MATE N LOC CONNECTOR LOCATION.

DESIGN INFORMATION
SURFACE MOUNT TECHNOLOGY: SIDE 1 _X_ SIDE 2 _X_ NONE ___

APPROVALS		DATE
DRAWN:	J. CUNNINGHAM	27JUL94
CHECKED:	W. SUPINSKI	27JUL94
MECH ENG:		
PROJ ENG:		
MFG ENG:		
SCALE: 2/1		
SHEET 1	OF 5	
TOP DOC NO:	K-DD-5423332-0-0	

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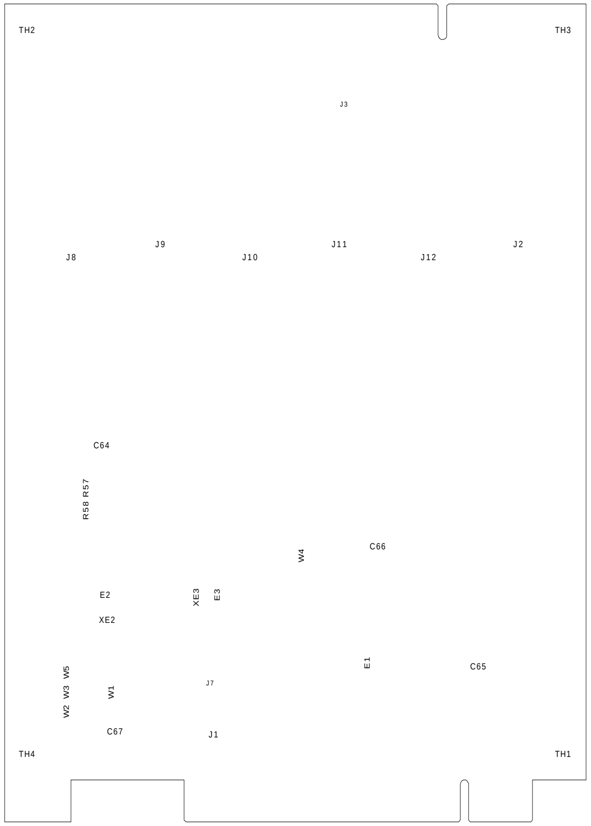
TITLE: PPB EVAL BOARD

SIZE	CODE	NUMBER	REV
D	UA	5423332-0-0	AX04

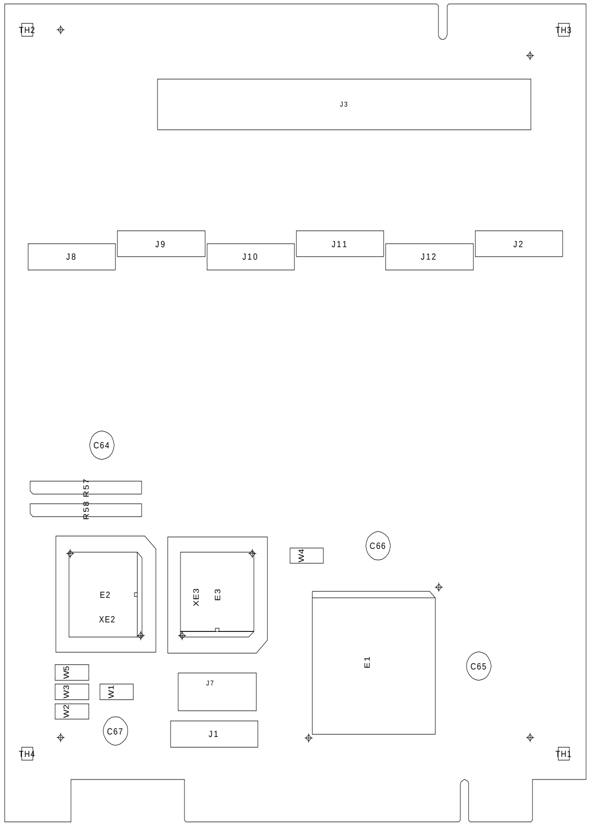
ETCH REV C1 MEDIA= K

WO# 298173

VIEWED FROM SIDE 1



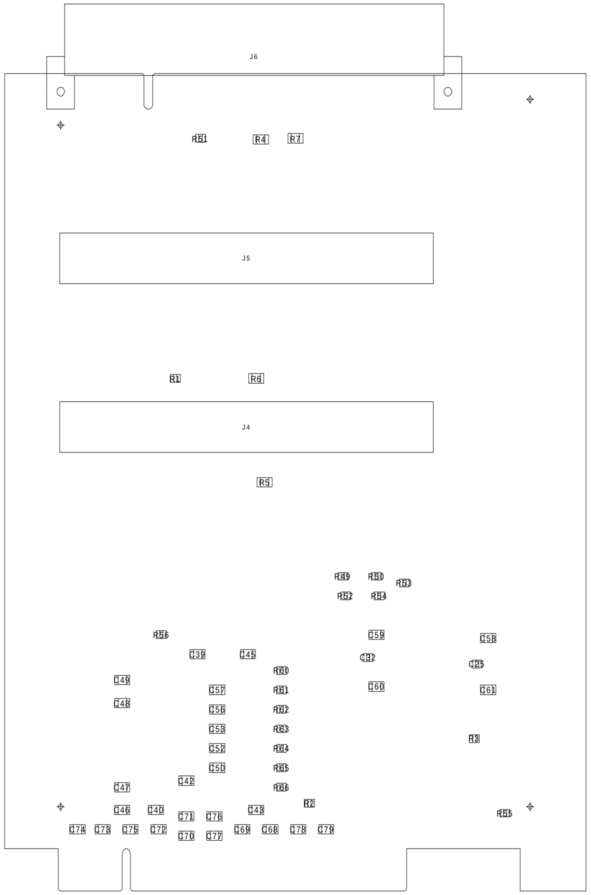
VIEWED FROM SIDE 1



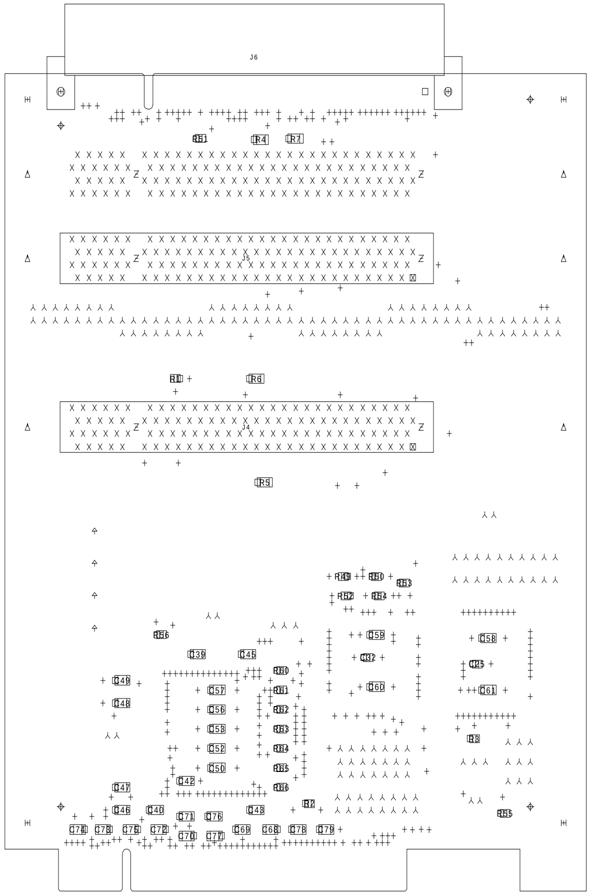
TITLE PPB EVAL BOARD	DOCUMENT NUMBER			
	SIZE D	CODE UA	NUMBER 5423332-0-0	REV AX04
	SCALE 2/1	MEDIA= K	SHEET 2 OF 5	

WO# 298173

VIEWED FROM SIDE 2



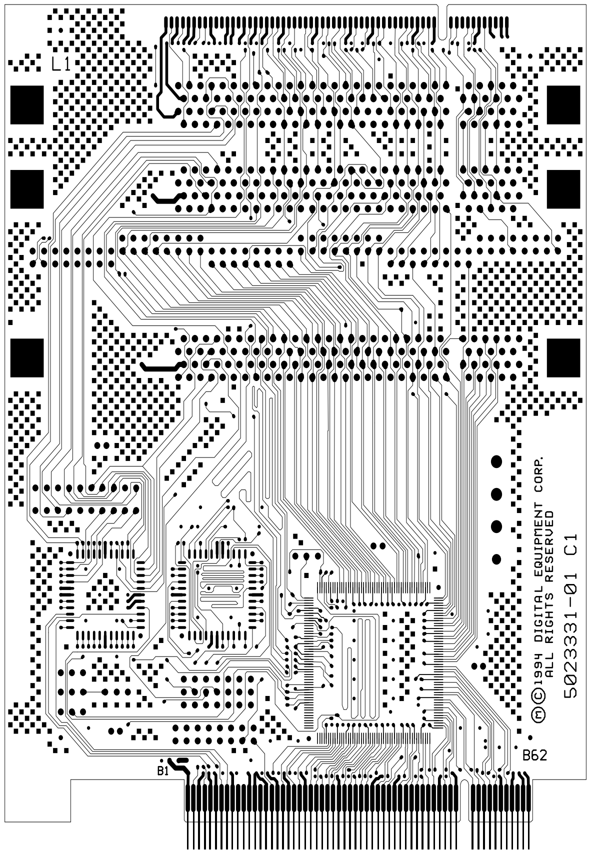
VIEWED FROM SIDE 2



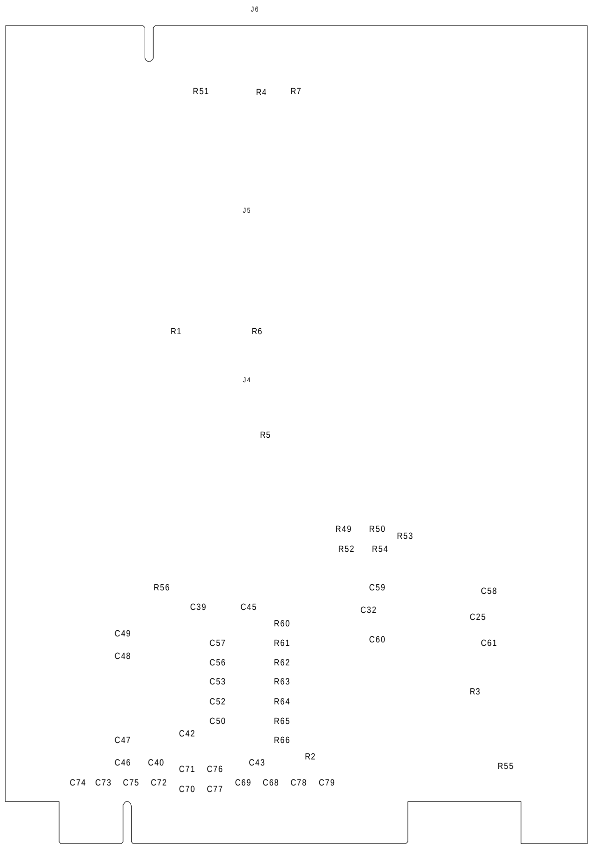
TITLE	DOCUMENT NUMBER			
PPB EVAL BOARD	SIZE: D	CODE: UA	NUMBER: 5423332-0-0	REV: AX04
	SCALE 2/1	MEDIA= K	SHEET 3 OF 5	

1 WO# 298173

LAYER 1 VIEWED FROM SIDE 1



VIEWED FROM SIDE 2



TITLE	DOCUMENT NUMBER			
	SIZE	CODE	NUMBER	REV
PPB EVAL BOARD	D	UA	5423332-0-0	AX04
	SCALE 2/1	MEDIA= K	SHEET 4 OF 5	

1 WO# 298173

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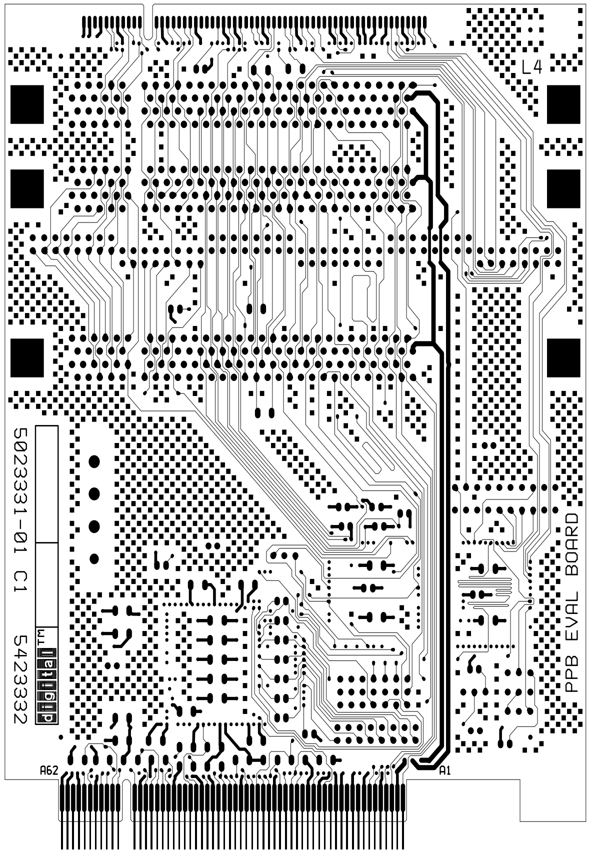
LAYER 4 VIEWED FROM SIDE 2



TITLE	DOCUMENT NUMBER			
PPB EVAL BOARD	SIZE	CODE	NUMBER	REV
	D	UA	5423332-0-0	AX04
	SCALE 2/1	MEDIA= K	SHEET 5 OF 5	

1 WO# 298173

D UA 5423332-0-0 AX04

D UA 5423332-0-0 AX04