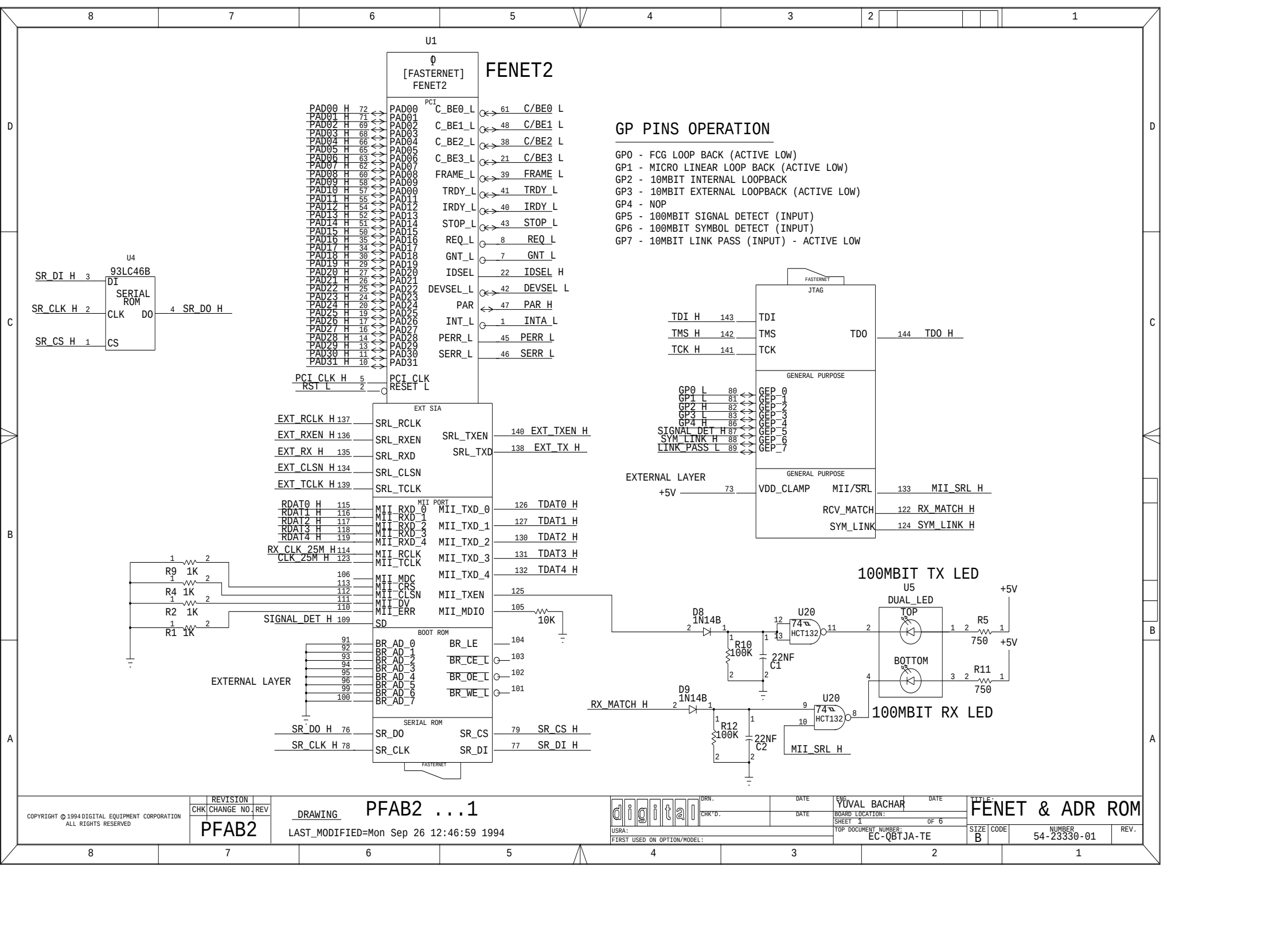

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REVISION		
CHK	CHANGE NO.	REV
PFAB2		

DRAWING PFAB2 ...1

LAST_MODIFIED=Mon Sep 26 12:46:59 1994

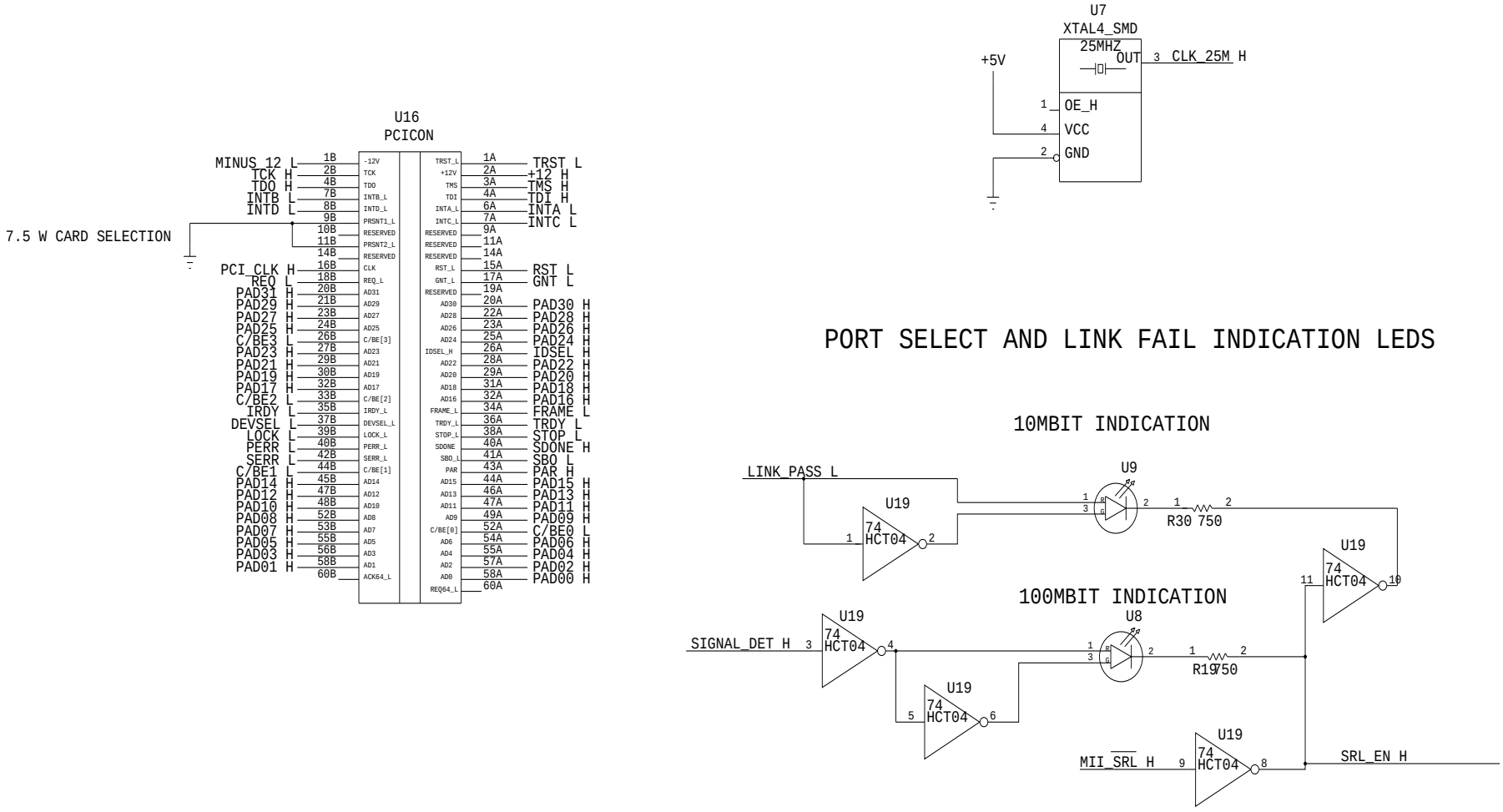
digital

DRN. DATE
CHK'D. DATE
USRA:
FIRST USED ON OPTION/MODEL:

ENG. YUVAL BACHAR DATE
BOARD LOCATION:
SHEET 1 OF 6
TOP DOCUMENT NUMBER: EC-QBTJA-TE

TITLE: FENET & ADR ROM

SIZE	CODE	NUMBER	REV.
B		54-23330-01	



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REVISION		
CHK	CHANGE NO.	REV
PFAB2		

DRAWING PFAB2 ...2

LAST_MODIFIED=Mon Sep 26 12:47:16 1994

DRN.	DATE	ENG. YUVAL BACHAR DATE
CHK'D.	DATE	BOARD LOCATION:
USRA:		SHEET 2 OF 6
FIRST USED ON OPTION/MODEL:		TOP DOCUMENT NUMBER: EC-QBTJA-TE

TITLE: PCI INTERFACE

SIZE	CODE	NUMBER	REV.
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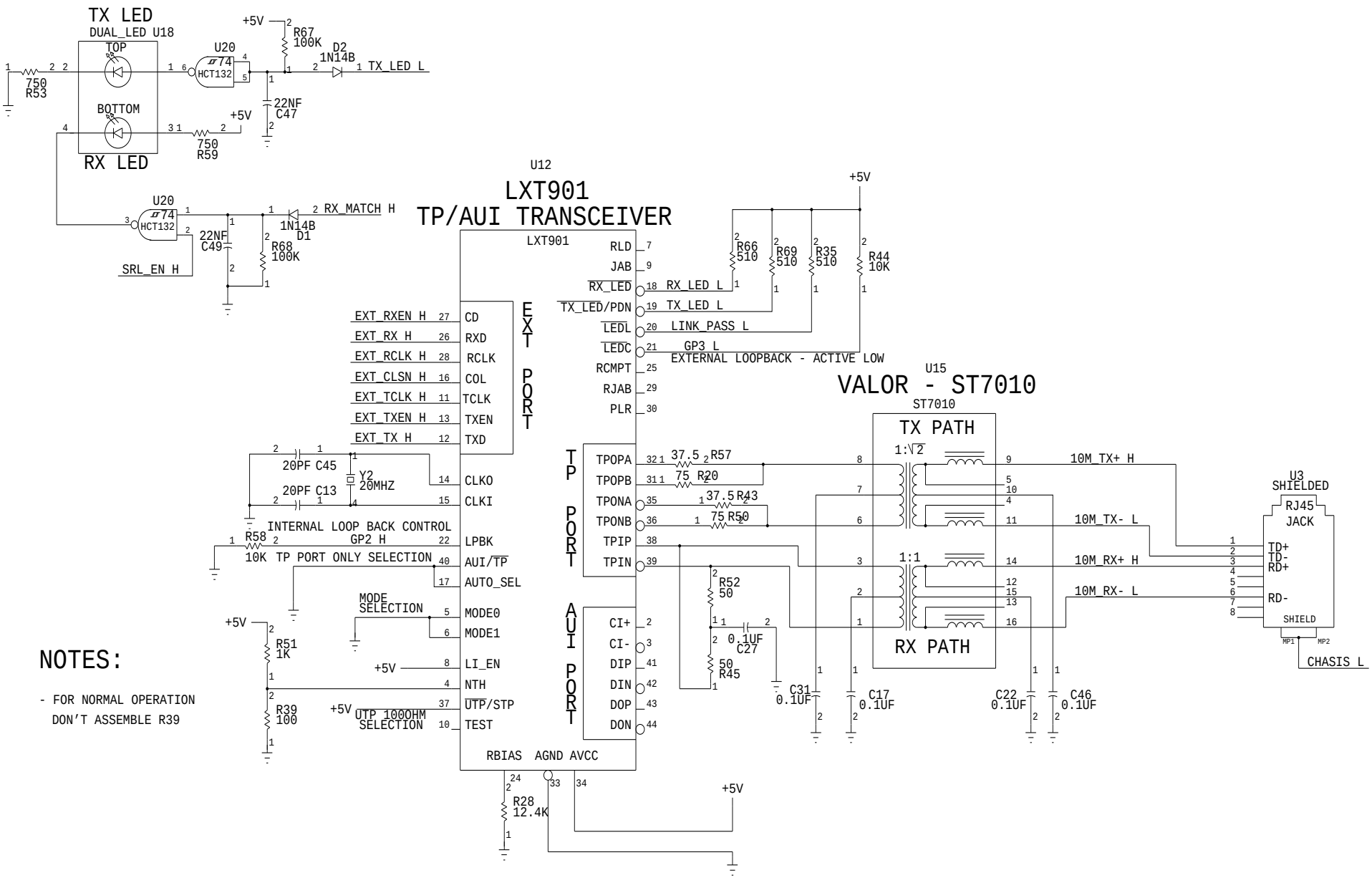

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REVISION		
CHK	CHANGE NO.	REV
PFAB2		

DRAWING PFAB2 ...3

LAST_MODIFIED=Thu Oct 6 16:02:20 1994

digital

DRN. DATE
CHK'D. DATE
USRA:
FIRST USED ON OPTION/MODEL:

ENG. YUVAL BACHAR DATE
BOARD LOCATION:
SHEET 3 OF 6
TOP DOCUMENT NUMBER: EC-QBTJA-TE

TITLE: 10MBIT TP INT.

SIZE	CODE	NUMBER	REV.
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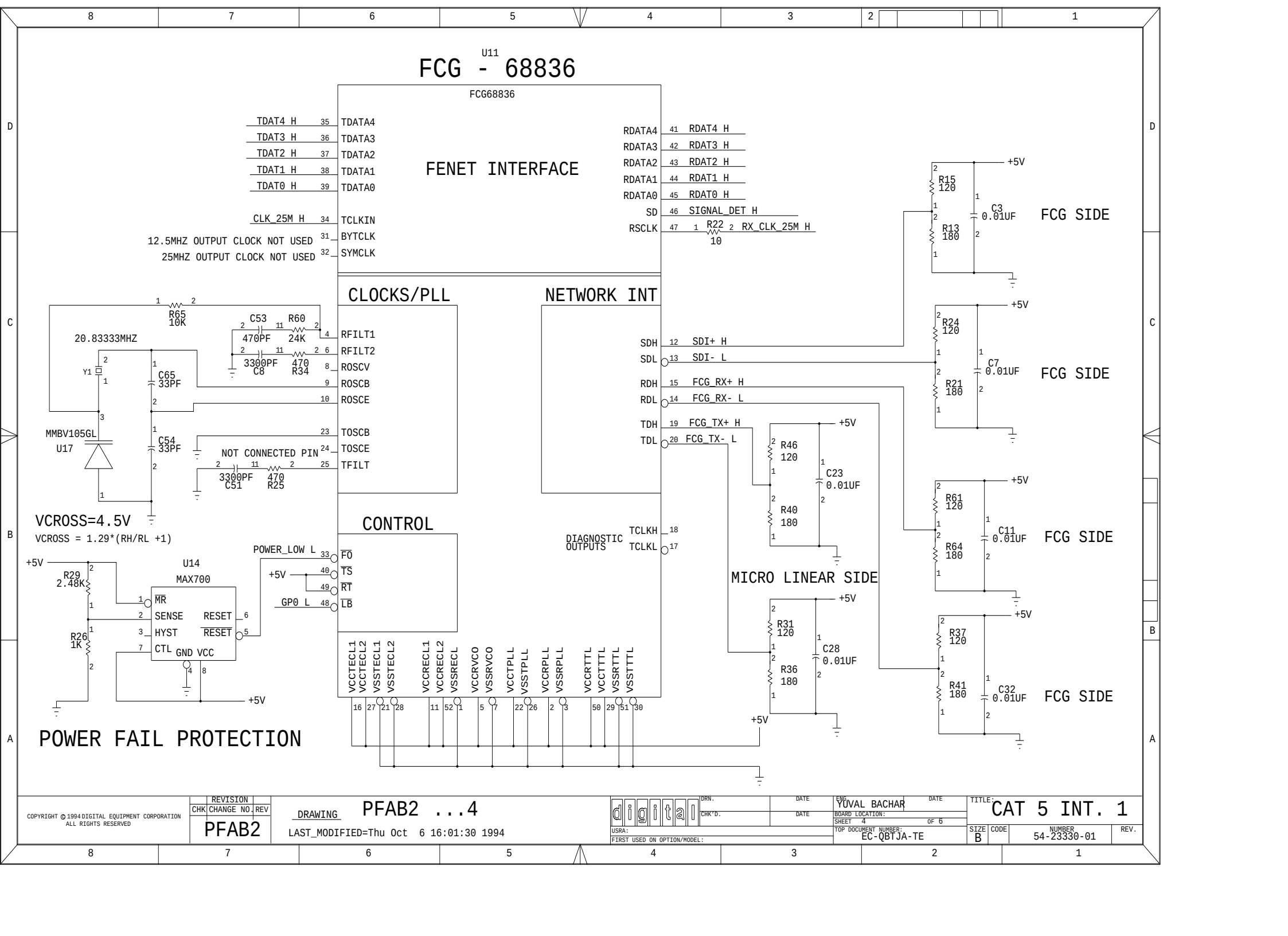

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REVISION		
CHK	CHANGE NO.	REV
PFAB2		

DRAWING PFAB2 ...4

LAST_MODIFIED=Thu Oct 6 16:01:30 1994

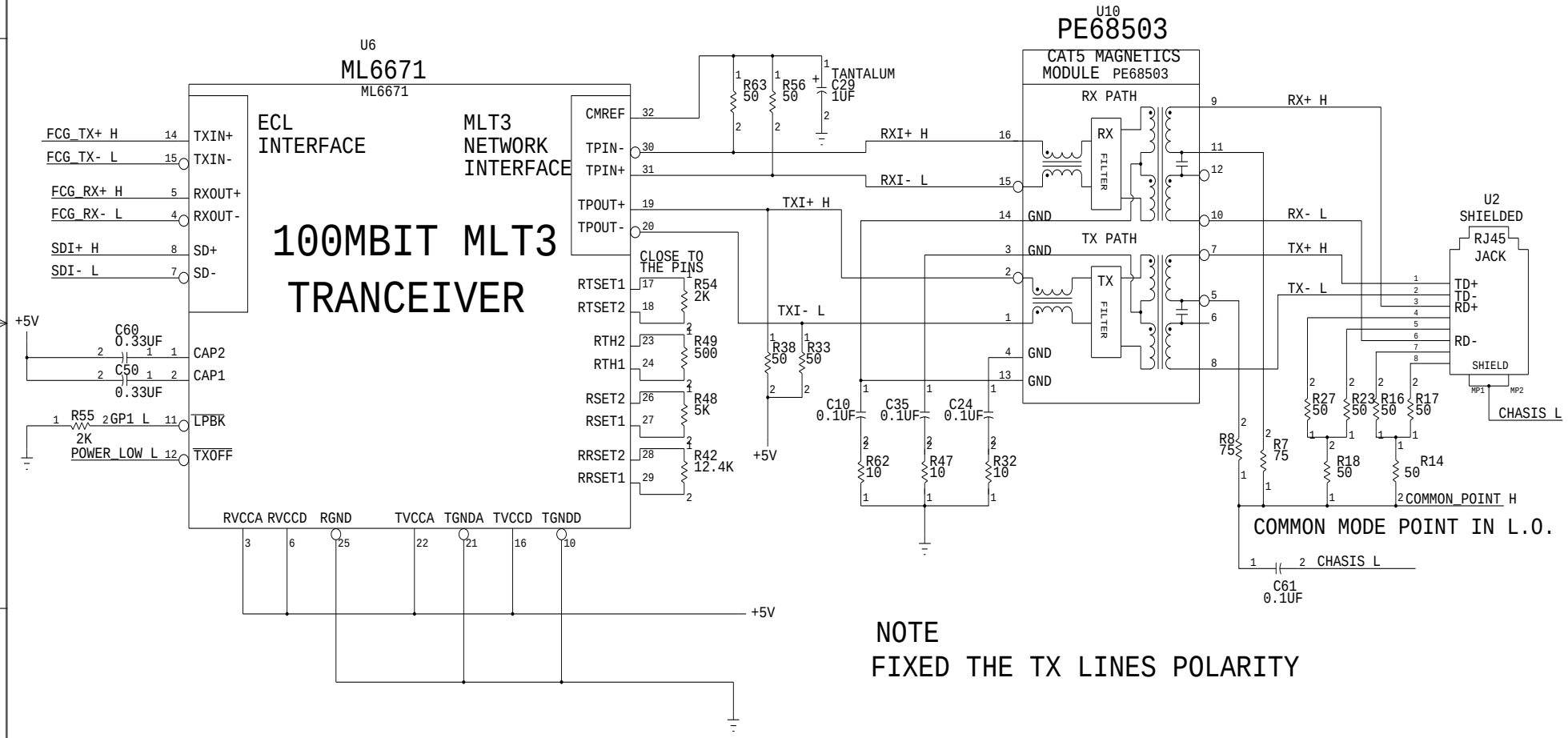
digital

DRN. DATE
CHK'D. DATE
USRA:
FIRST USED ON OPTION/MODEL:

ENG. YUVAL BACHAR DATE
BOARD LOCATION:
SHEET 4 OF 6
TOP DOCUMENT NUMBER: EC-QBTJA-TE

TITLE: CAT 5 INT. 1

SIZE	CODE	NUMBER	REV.
B		54-23330-01	



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REVISION		
CHK	CHANGE NO.	REV
PFAB2		

DRAWING PFAB2 ...5

LAST_MODIFIED=Thu Oct 20 11:56:32 1994

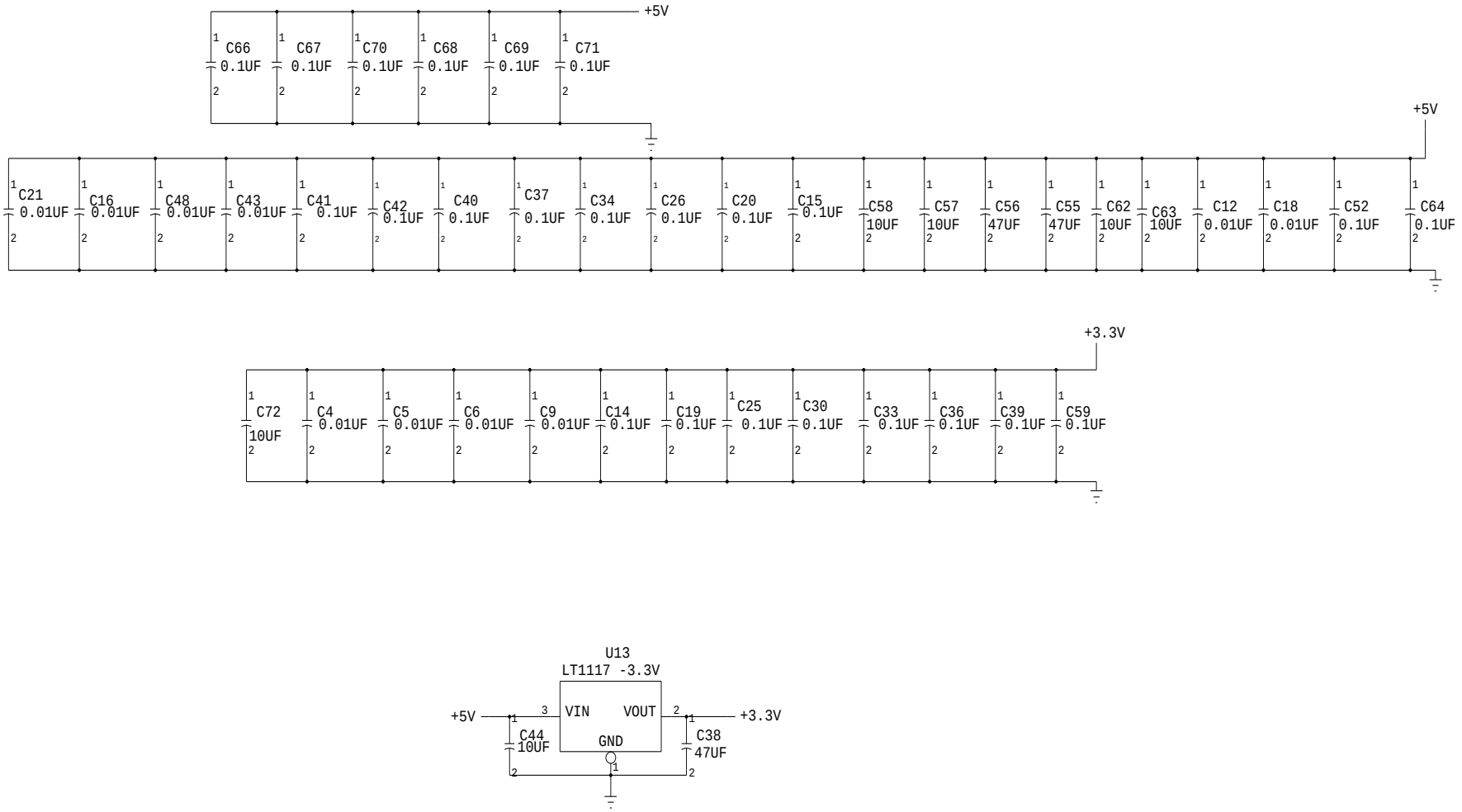
digital

DRN. DATE
CHK'D. DATE
USRA:
FIRST USED ON OPTION/MODEL:

ENG YUVAL BACAHR DATE
BOARD LOCATION:
SHEET 5 OF 6
TOP DOCUMENT NUMBER: EC-QBTJA-TE

TITLE: CAT 5 INT. 2

SIZE	CODE	NUMBER	REV.
B		54-23330-01	



3.3 VOLTAGE REG.

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REVISION		
CHK	CHANGE NO.	REV

PFAB2

DRAWING

PFAB2 ...6

LAST_MODIFIED=Thu Oct 6 16:07:39 1994

digital

DRN. DATE

CHK'D. DATE

USRA:

FIRST USED ON OPTION/MODEL:

ENG. YUVAL BACHAR DATE

BOARD LOCATION:

SHEET 6 OF 6

TOP DOCUMENT NUMBER: EC-QBTJA-TE

TITLE: CUPLING CAPS

SIZE	CODE	NUMBER	REV.
B		54-23330-01	

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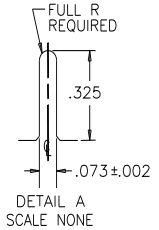
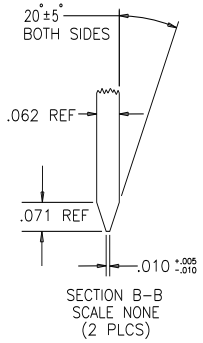
VIEWED FROM SIDE 1

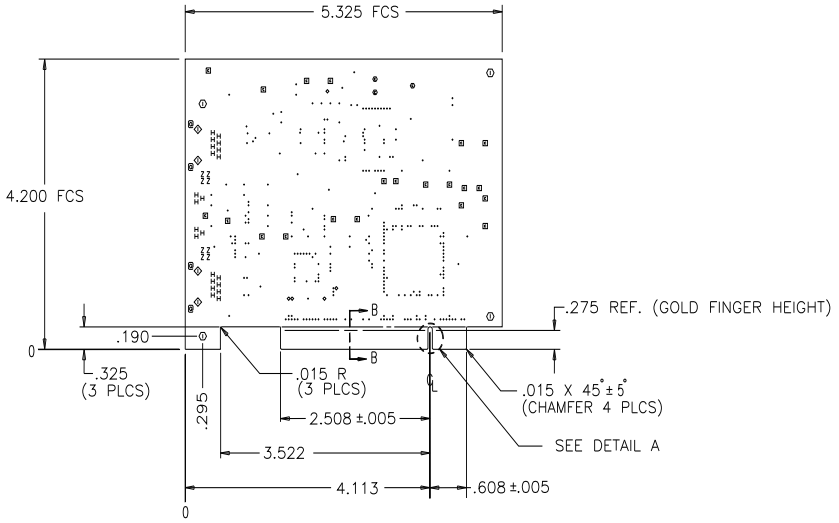
DATE	RESPONSIBLE ENGINEER	ECO NUMBER	REV





NOTES:

1. IF A CONFLICT BETWEEN THIS DOCUMENTATION/DESIGN PACKAGE AND ANY OTHER REFERENCED DOCUMENT EXISTS, THIS DOCUMENT /DESIGN PACKAGE WILL TAKE PRECEDENCE.
2. ALL ARTWORK FEATURES, IN BOTH DATA AND FILM FORM, ARE SUPPLIED AT NOMINAL DIMENSIONS. UNLESS OTHERWISE SPECIFIED, THE FINISHED CIRCUIT SHOULD REFLECT THESE DIMENSIONS INCLUDING ALLOWABLE FABRICATION TOLERANCES PER DEC STD-176.
   SOLDERMASK FEATURES ARE SUPPLIED AT MINIMUM SIZES REFLECTING DESIRED COVERAGE. THESE FEATURES MAY BE MODIFIED TO ACCOMMODATE SPECIFIC PROCESSES. REFERENCE DEC STD-176 FOR ASSOCIATED REQUIREMENTS.
3. ALL .015 DIA. HOLES TO BE DRILLED .015 DIA. MIN.
4. DIELECTRIC MATERIAL TO BE FR4 WITH A MINIMUM GLASS TRANSITION TEMPERATURE OF 125 C.
5. ALL COPPER FOIL SHALL MEET OR EXCEED ANSI/IPC CF-150 TYPE ED (ELECTRODEPOSITED) CLASS 1 REQUIREMENTS.
6. NICKEL AND GOLD PLATE FINGER SET AREA PER DIGITAL STANDARD 176. VENDOR TO ADD PLATING BUSS BAR FOR EACH CIRCUIT.
7. LIQUID PHOTOIMAGABLE SOLDERMASK (LPISM) IS REQUIRED ON BOTH SIDES OF THE PWB. NO SOLDERMASK WILL BE PERMITTED ON THE TOP (SOLDERABLE SURFACE) OF ANY SURFACE MOUNT DEVICE PADS.



PRINTED WIRE BOARD LAYER CONSTRUCTION

LAYER	DIELECTRIC SPACING	LAYER TYPE	COPPER WEIGHT	LINE WIDTH(S)
1		SIGNAL	0.5 oz.+PLATING	.006,.008,.010
	\\\\ 0.010 +/- 0.0015 \\\\			
2		PLANE	0.5 oz.	
	\\\\ 0.015 +/- 0.0015 \\\\			
3		SIGNAL	0.5 oz.	.006,.008
	\\\\ 0.010 +/- 0.0015 \\\\			
4		SIGNAL	0.5 oz.	.006,.008
	\\\\ 0.015 +/- 0.0015 \\\\			
5		PLANE	0.5 oz.	
	\\\\ 0.010 +/- 0.0015 \\\\			
6		SIGNAL	0.5 oz.+PLATING	.006,.008,.010

OVERALL THICKNESS SPECIFICATION=0.062 +/- 0.007 (PLATING-TO-PLATING)
0.5 oz. COPPER FOIL THICKNESS NOMINAL: .0007 (.0003 MIN. AFTER PROCESSING)

OUTER LAYERS ARE STARTED WITH 0.5 oz. COPPER AND PLATED TO 0.0014 MINIMUM COPPER THICKNESS.
SIGNAL LINE WIDTH TOLERANCE = +/- 0.0015
MINIMUM COPPER REQUIREMENT FOR PLATED THRU HOLE WALLS IS 0.0008

HOLE DATA (FOR STANDARD HOLE TOLERANCES USE DEC STD 176)

SYMBOL	•	◇	⊠	⊕	H	Z	@	Ⓘ	◈
FINISHED HOLE SIZE (INCHES)	.015	.020	.030	.032	.038	.042	.062	.125	.128
POSITIVE TOLERANCE (INCHES)	.003								
NEGATIVE TOLERANCE (INCHES)	.015								
FINISHED HOLE SIZE (mm)									
POSITIVE TOLERANCE (mm)									
NEGATIVE TOLERANCE (mm)									
PLATED	Y	Y	Y	Y	Y	Y	Y	N	N
QUANTITY	287	5	21	3	22	8	4	4	4

FROM LAYER 1 TO LAYER 6

FABRICATION INFORMATION: MATERIAL AND WORKMANSHIP FOR ALL FABRICATED PRINTED WIRING BOARDS MUST MEET THE REQUIREMENTS OF DEC STD 176

FABRICATE BOARD PER: _____ AS SHOWN X | STEP AND REPEAT CODE: NONE | PRESS FIT COMPONENTS: YES ___ NO X

DESIGN INFORMATION

NUMBER OF LAYERS: 6 LAYER CONSTRUCTION: _____ AS SHOWN X
CONTROLLED IMPEDANCE: YES ___ NO X CONTROLLED CAPACITANCE: YES ___ NO X
SURFACE MOUNT TECH / MINIMUM PITCH: SIDE 1 .025 SIDE 2 >.050
PLANAR RESISTOR TECHNOLOGY: YES ___ NO X NUMBER OF PRT LAYERS ___
BOARD FINISH: REFLOW TIN LEAD ___ HASL X ALL COPPER ___ OTHER ___
SOLDERMASK: NONE ___ SIDE 1 X SIDE 2 X | SMOBC: YES X NO ___
NOMENCLATURE: NONE ___ SIDE 1 X SIDE 2 X | TENTED VIAS: YES ___ NO X
MINIMUM VIA HOLE SIZE: .015 MINIMUM COMPONENT HOLE SIZE: .032
PAD EXPANSION: OUTER LAYERS X SIGNAL INNER LAYERS X NONE ___
INCIRCUIT THIEVING: NONE ___ SIDE 1 X SIDE 2 X AS SHOWN ___
TECHNOLOGY (LW/LS): LINE .006 SPACE .007
CIRCUIT OUTLINE/FINGER DETAIL: _____ AS SHOWN X
SPECIAL REQUIREMENTS: _____

TOLERANCES
INCHES UNLESS OTHERWISE SPECIFIED

INCHES	mm	ANGLES
.XXX ± .010	.XX ± .25	±0 DEG
.XX ± .020	.X ± .50	30 MIN
.X ± .100	X, ± 2,50	

APPROVALS	DATE
DRAWN: STEVE BAKER	12-OCT-94
CHECKED:	
MECH ENG:	
PROJ ENG:	
MFG ENG:	
SCALE: 1:1	
SHEET 1 OF 1	
TOP DOC NO: K-DD-5023329-0-0	

digital™

TITLE:
CIRCUIT DRILL AND ETCH DRAWING

SIZE	CODE	NUMBER	REV
D	MD	5023329-0-0	AX00

ETCH REV: A01 | MEDIA K

WO# _____

REV	NUMBER	CODE	SIZE
AX00	5023329-0-0	MD	D

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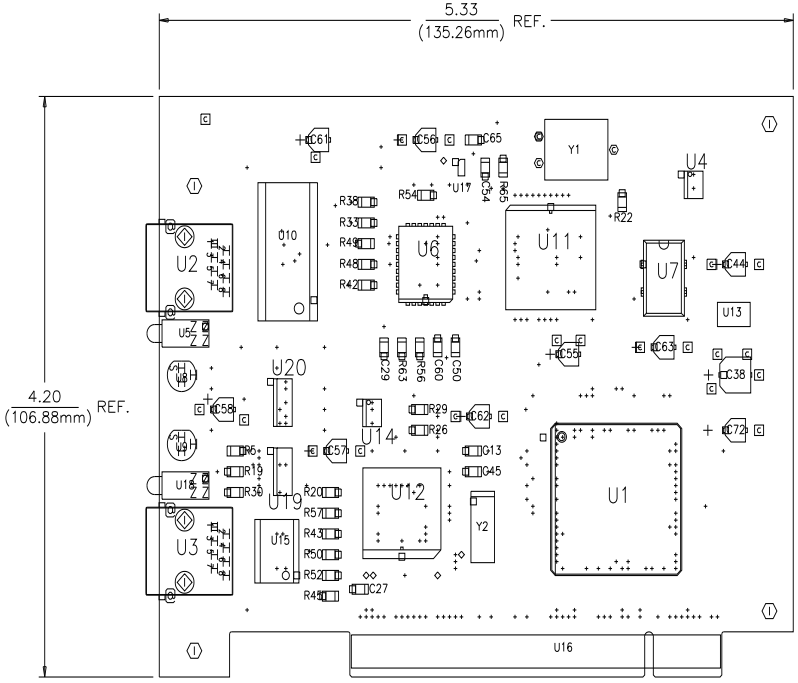
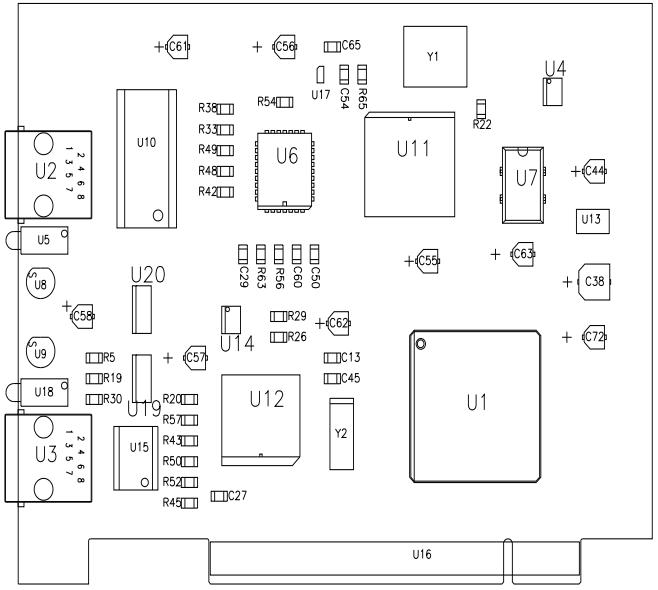
VIEWED FROM SIDE 1





DATE	RESPONSIBLE ENGINEER	ECO NO	REV	DATE	RESPONSIBLE ENGINEER	ECO NO	REV

NOTES:

DESIGN INFORMATION

SURFACE MOUNT TECHNOLOGY: SIDE 1 X SIDE 2 X NONE

APPROVALS	DATE
DRAWN: STEVE BAKER	13-OCT-94
CHECKED:	
MECH ENG:	
PROJ ENG:	
MFG ENG:	
SCALE: 2:1	
SHEET 1 OF 3	
TOP DOC NO: K-DD-5423330-0-0	

digital™

TITLE: PFAB2
ASSEMBLY

SIZE	CODE	NUMBER	REV
D	UA	5423330-0-0	AX00

ETCH REV A01 | MEDIA K

WO# _____

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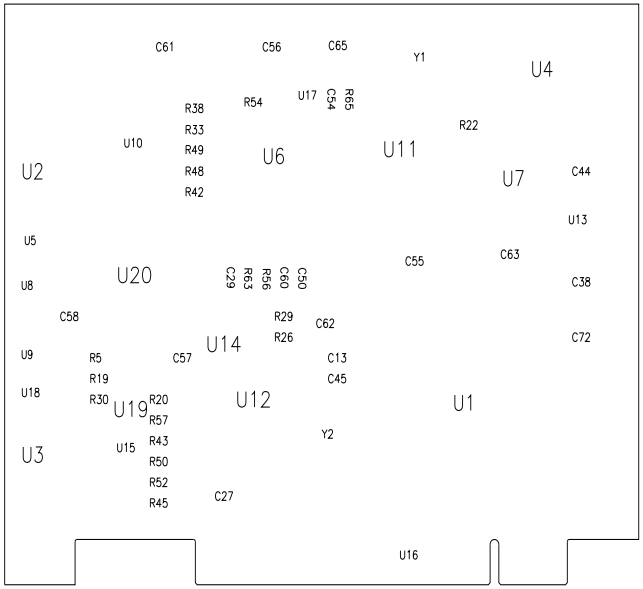

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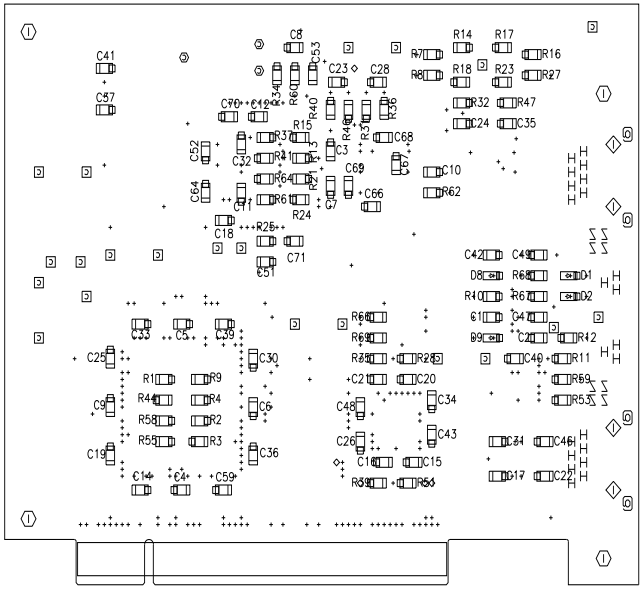

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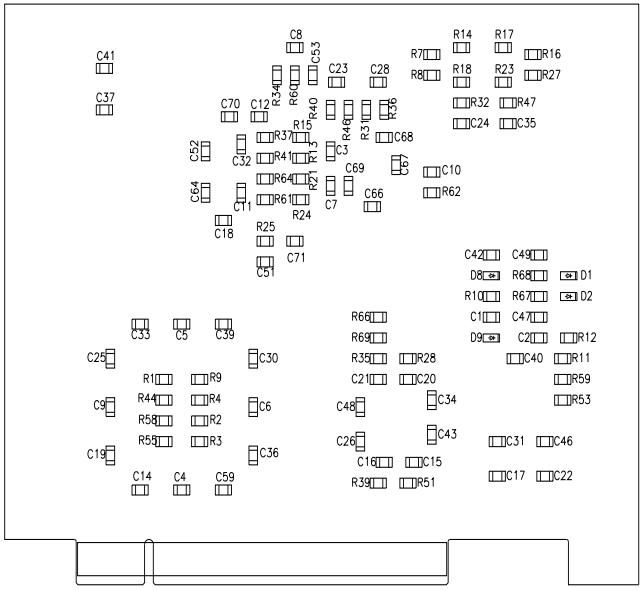

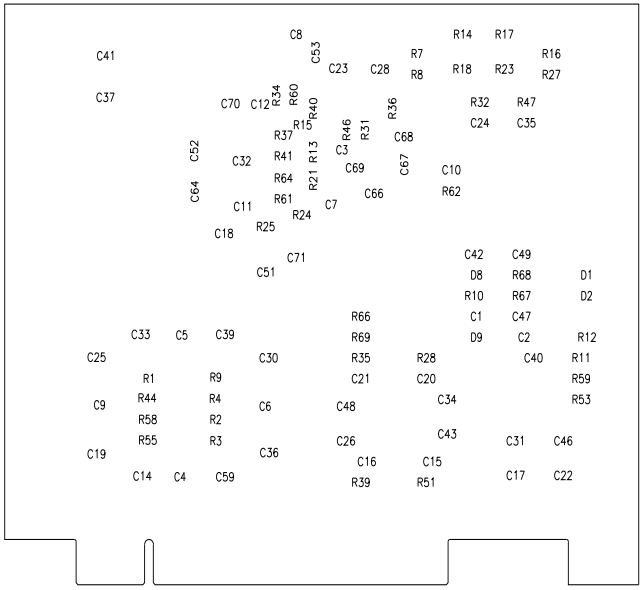

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